

**AMENDMENTS TO THE SPECIFICATION**

Please change the title of the application to:

Liquid Crystal Display Having Passivation Layer Partially Exposing Gate Insulating Layer

Please replace the paragraph on page 11 at lines 7-11 with the following paragraph:

Referring to FIG. 5, a thin film transistor is constituted where the gate and data lines ~~55L and 51L~~ 51L and 55L respectively cross each other. The TFT comprises a source electrode 51S, a gate electrode ~~[[55G]]~~ 51G which is a portion of the gate line ~~[[55L]]~~ 51L, a drain electrode ~~[[51D]]~~ 55D standing face to face opposite the source electrode 51S, and an active layer 53 overlapped with these electrodes. The drain electrode 55D is connected to a first pixel electrode 57.